

Combinatorial and Sequencing Control

Chapter 13

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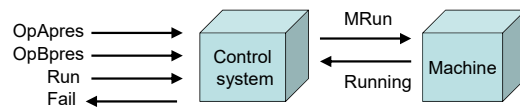
Combinatorial vs. Sequential

- Combinatorial – no memory
 - Only depends on current signals
- Sequential – contains state(s)
 - Next state is a function of:
 - Current inputs
 - Current state

Applicable for:
Machine and/or Control system

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Example: Combinatorial Control



Task:

Run the machine while the "Run" signal is given if both operator A and operator B are present.

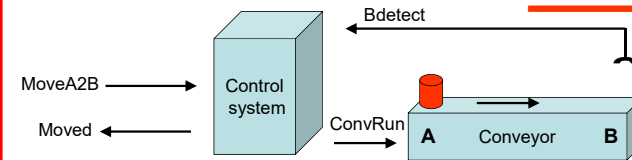
Give "Fail"-signal if machine doesn't respond.

$MRUN = OpApres \text{ AND } OpBpres \text{ AND } Run$

$Fail = MRUN \text{ AND NOT } Running$

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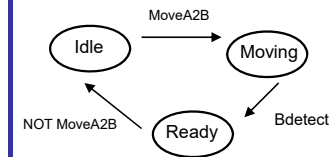
Example: Sequential Control



Task:

Move the object from A to B when the signal "MoveA2B" becomes true.

Give a true signal on "Moved" when the object has reached B.



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Consequently....

- The controlled machine may contain states
- The controller may contain states
- The macro view of controller and machine contain states if any subsystem contain states

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Focus on the Controller

- Most controllers are sequential
- Is this a sequential controller? :

```
while (true) {  
  while (NOT MoveA2B) /* do nothing */ ;  
  ConvRun := true;  
  while (NOT Bdetect) /* do nothing */ ;  
  ConvRun := false;  
  Moved := true;  
  while (MoveA2B) /* do nothing */ ;  
  Moved := false;  
}
```

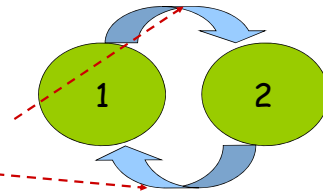
If yes – where is the state?

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Event Driven Systems – State Concept

Sequencing circuits

- one state at a time
- state transfer
- conditions for each state transfer



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Sequential Control Tools

- State diagrams
- Petri nets
- Sequential Function Charts - Grafcet
- Switching theory
- PLC languages IEC61131

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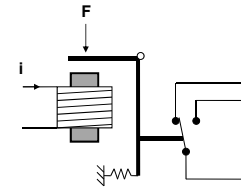
PLC programming

The standard IEC 61131-3 implies:

- definition of sequential function chart (SFC), and 4 language options
 - instruction list (IL)
 - function block (FBD)
 - ladder diagram (LD)
 - structured text (ST)

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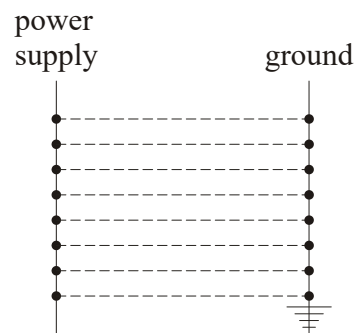
Automation in the Good Old Days....



Ladder (LD) is based on this model

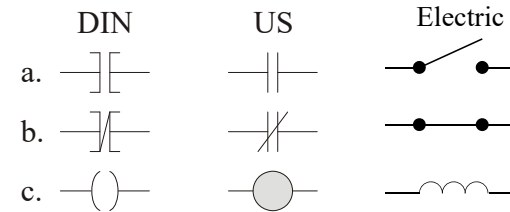
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Ladder Framework



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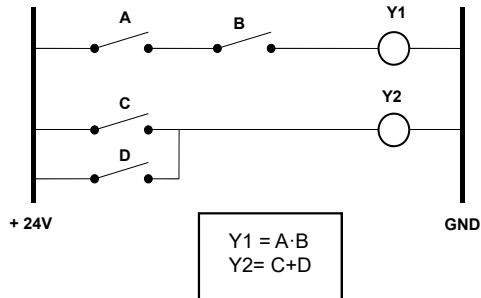
Ladder Symbols



a.) Normally open (NO), make
 b.) Normally closed (NC), break
 c.) Output, coil

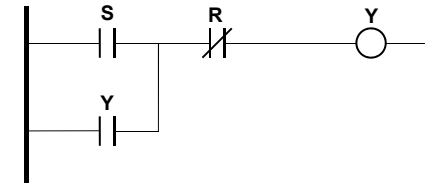
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Boolean Logic in LD



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Set – Reset in LD (State implementation)



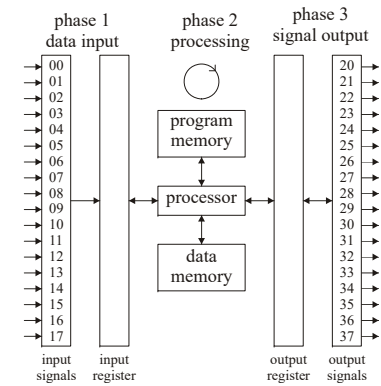
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Reflections...

- A relay has only one coil, consequently we use only one coil symbol with the same name.
- An arbitrary number of make/break elements can be used in a ladder diagram.
- Symbols may be physical or logical in a PLC.

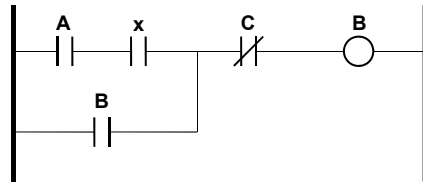
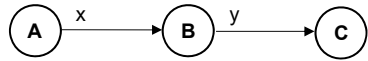
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An elementary PLC structure



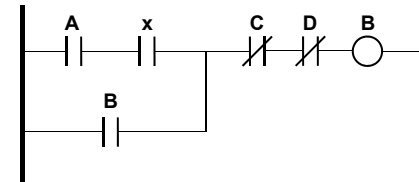
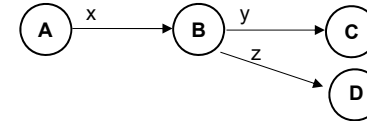
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State Encoding in LD



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Alternative State Transitions

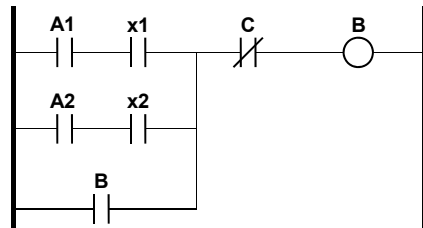
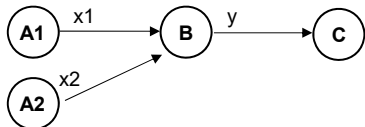


Note!

y and z have to be mutually excluding

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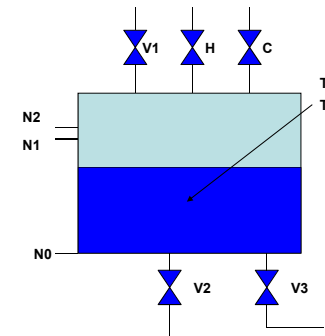
Several Ways to Enter a State



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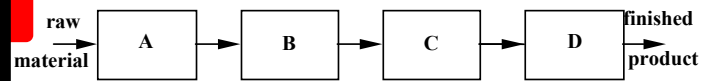
Example:

A mixing tank is used to produce process water with correct temperature (T1 true, T2 false). Supply water with approximately correct temperature is filled with V1 to level N2. If the temperature is too high or too low the water is reduced to N1 using dump valve V3. Then hot or cold water is added to level N2 using H or C. This is repeated until the temperature is correct. Then the water is delivered using V2 and a new batch of water should be produced.



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The Classical Transfer Line

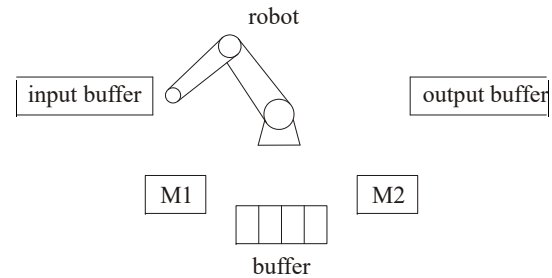


Starving - no input product

Blocking - can not deliver finished product

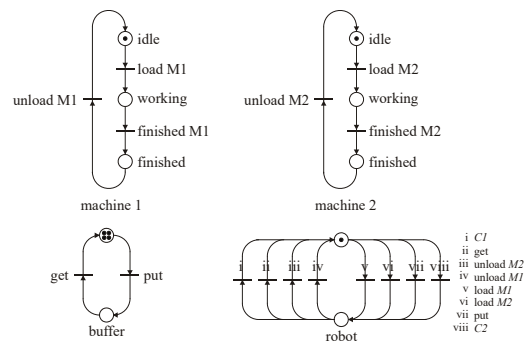
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Simple Cell



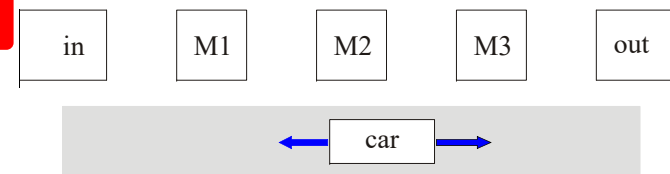
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Petri Net of the simple cell



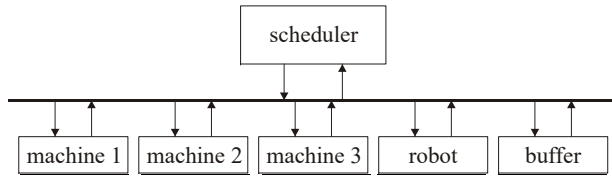
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The Manufacturing Cell



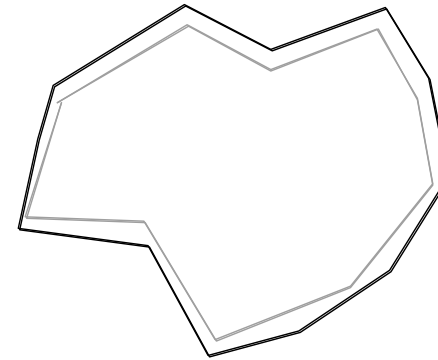
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Controlling the Cell



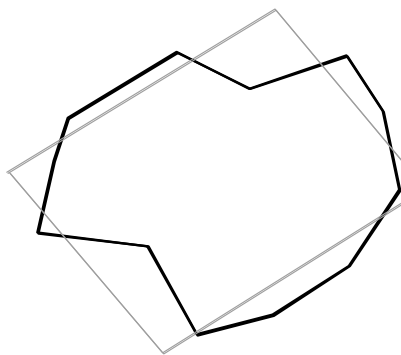
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1970s: à la carte programming



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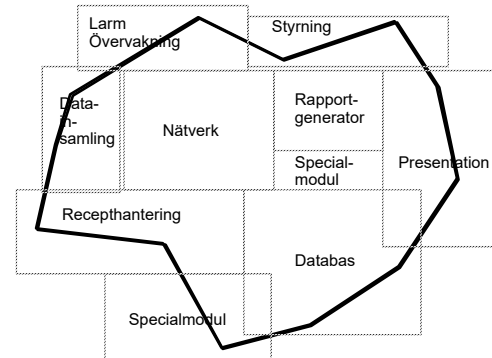
1980s: industrial packages



Exempel:
TDC3000
SCADA
SATT line

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1990s: open protocols



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The Software Problem

- Software ~2 yrs
- Hardware ~5 yrs
- Application ~15 yrs

Requires:

- Standardised interfaces (ActiveX, ODBC, DDE, OPC,...)

- Solutions have to survive a generation shift (planned obsolescence)
- Has to permit stepwise implementation

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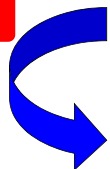
Two different developments

- The **process** industry
 - Replace the analog PID controller with a computer
 - The instrumentation people
- The **manufacturing** industry
 - Replace the electromechanical relays with a computer
 - The electricians

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Sequential Control Tools

- State diagrams ✓
- Petri nets ✓
- Sequential Function Charts – Grafcet (SFC)
- **Switching theory**
- PLC languages IEC61131 (LD ✓)



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| | DIN/IEC | US | truth table | boolean expression |
|------|---------|----|--|---|
| NOT | | | $\begin{array}{c c} u & y \\ \hline 0 & 1 \\ 1 & 0 \end{array}$ | $y = \bar{u}$ |
| OR | | | $\begin{array}{c cc} u_1 & u_2 & y \\ \hline 0 & 0 & 0 \\ 0 & 1 & 1 \\ 1 & 0 & 1 \\ 1 & 1 & 1 \end{array}$ | $y = u_1 + u_2$ |
| AND | | | $\begin{array}{c cc} u_1 & u_2 & y \\ \hline 0 & 0 & 0 \\ 0 & 1 & 0 \\ 1 & 0 & 0 \\ 1 & 1 & 1 \end{array}$ | $y = u_1 \cdot u_2$ |
| NOR | | | $\begin{array}{c cc} u_1 & u_2 & y \\ \hline 0 & 0 & 1 \\ 0 & 1 & 0 \\ 1 & 0 & 0 \\ 1 & 1 & 0 \end{array}$ | $y = \overline{u_1 + u_2}$ |
| NAND | | | $\begin{array}{c cc} u_1 & u_2 & y \\ \hline 0 & 0 & 1 \\ 0 & 1 & 1 \\ 1 & 0 & 1 \\ 1 & 1 & 0 \end{array}$ | $y = \overline{u_1 \cdot u_2}$ |
| XOR | | | $\begin{array}{c cc} u_1 & u_2 & y \\ \hline 0 & 0 & 0 \\ 0 & 1 & 1 \\ 1 & 0 & 1 \\ 1 & 1 & 0 \end{array}$ | $y = u_1 \cdot \bar{u}_2 + \bar{u}_1 \cdot u_2$ |
| Lock | | | | $y = u_1 \cdot u_2 \cdot \bar{S}$ |

Elementary Boolean expressions

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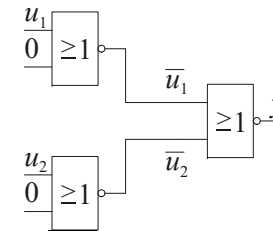
De Morgans theorem

$$\overline{(x \cdot y \cdot z \cdots)} = \bar{x} + \bar{y} + \bar{z} + \cdots$$

$$\overline{(x + y + z + \cdots)} = \bar{x} \cdot \bar{y} \cdot \bar{z} \cdots$$

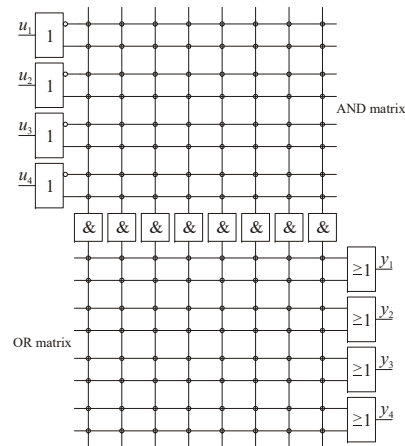
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Three NOR = AND



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input buffer
with inversion



Structure of
PLD, PROM

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PLC programming

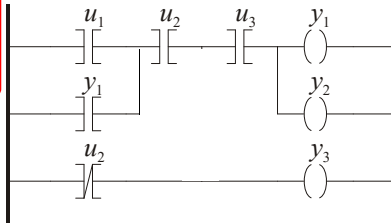
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From ladder to PLC code

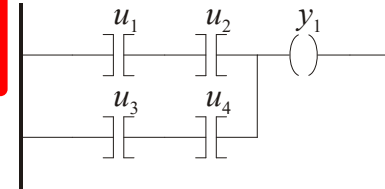
Instruction List (IL)



```
ld u1
or y1
and u2
and u3
out Y1
out Y2
ldi u2
out Y3
```

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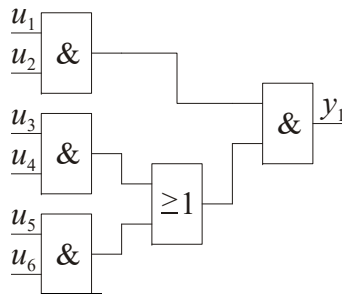
From ladder to PLC



```
ld u1
and u2
ld u3
and u4
orb
out Y1
```

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From Boolean to PLC



```
ld u1
and u2
ld u3
and u4
ld u5
and u6
orb
anb
out Y1
```

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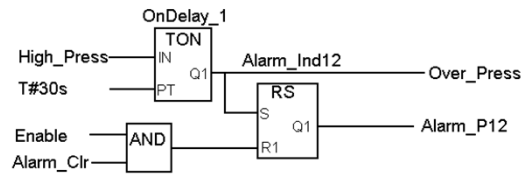
PLC programming

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Function Block Diagram (FBD)



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PLC programming

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Structured Text (ST)

```
IF SPEED1 > 100.0 THEN
  Flow_Rate := 50.0 + Offset_A1;
ELSE
  Flow_Rate := 100.0; Steam := ON;
END_IF;
```

Strongly resembles Pascal

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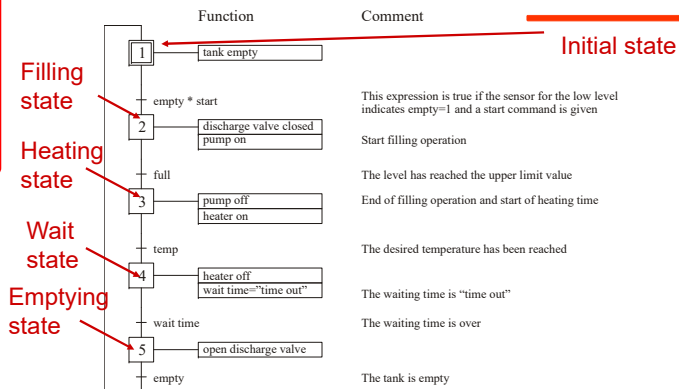
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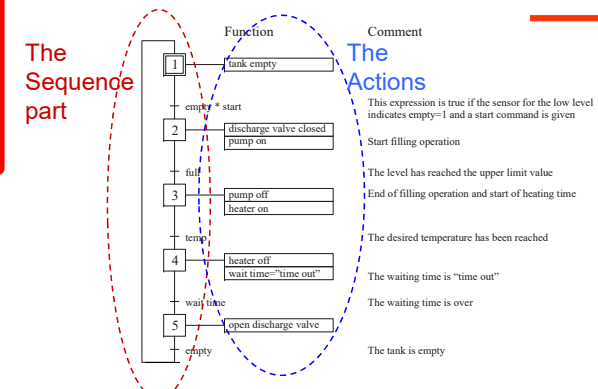
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Controlling a simple Batch Tank



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The Sequential Function Chart



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The finite state concept

- The process is represented by **5 states**
- Only **1 state** at a time!
- A **transition** signal marks the change from one state to another one
- A **condition** for transition (from a sensor, a timer, an operator)
- In each state there is an **action**

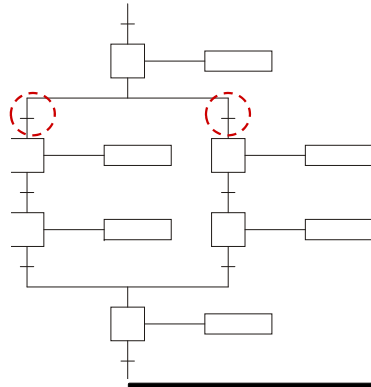
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Grafcet

- Developed in France 1977
- Graf + AFCET (Association Francaise pour la Cybernetique Economique et Technique)
- French standard 1982
- IEC standard 1988 - **IEC 848** (=SFC, Sequential Function Chart)
- Essential part of **IEC 61131-3**

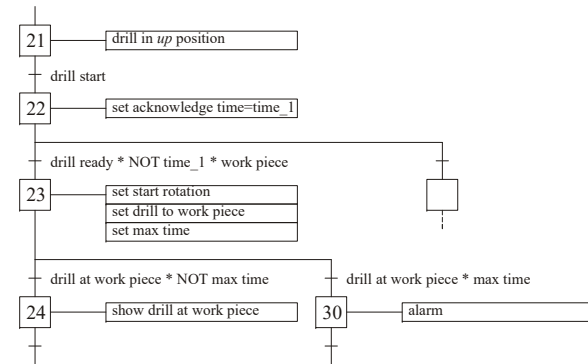
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Alternating Parallel Branches



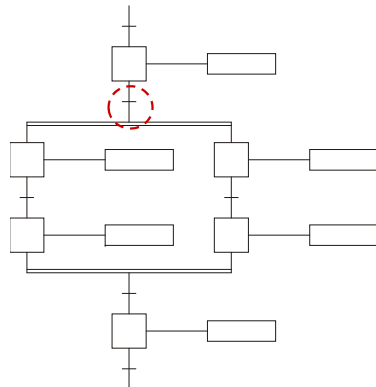
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Grafset for a Drill (Alternating parallel branches)

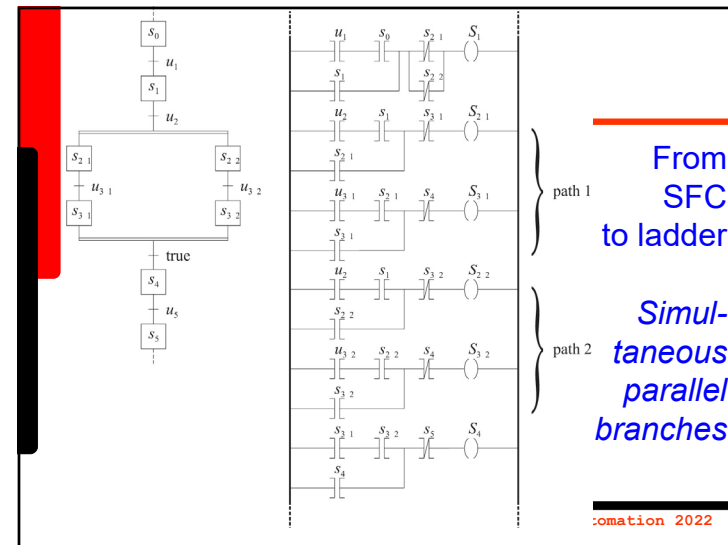


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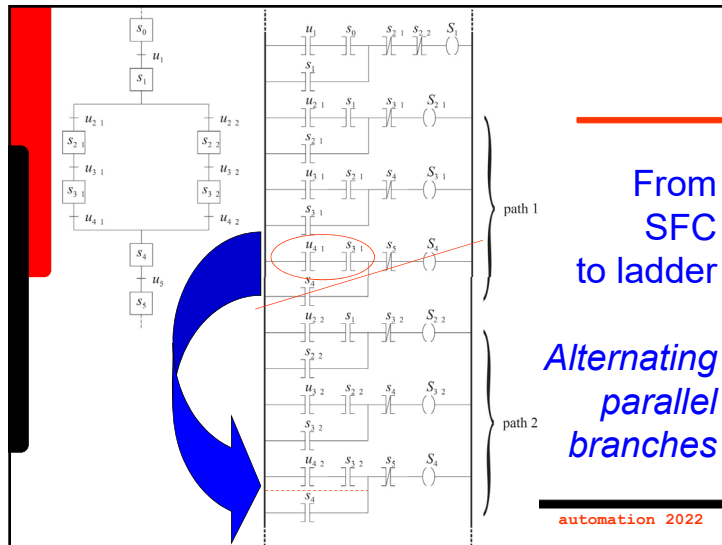
Simultaneous Parallel Branches



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PLC open

- PLC open is an organisation to support the standard **61131-3**
- Manufacturer and product independent
- Members of the PLC open group are expected to deliver products that follow the 61131-3 standard

<http://www.plcopen.org/>

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PLC Open (2)

- **Open systems** can be replaced with software or hardware from a third party
- Some companies call products open incorrectly
- The IEC 61131 standard encourages **interchangeable** systems
- Open architecture controllers **replace** a PLC with a standard computer

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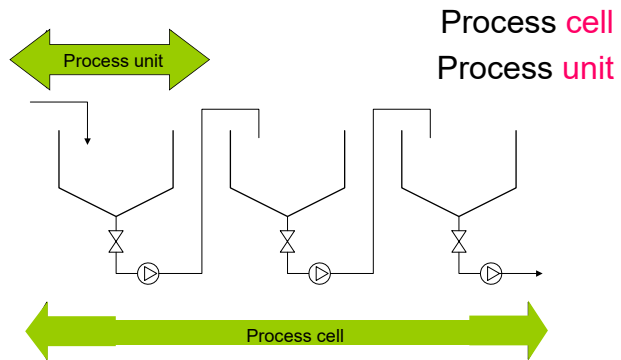
Batch Operation

- Batch operation software is standardised in the ISA **S-88** standard (ISA)
- S-88 defines **models** and **terminology**

www.isa.org/isa88

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Structure of a Batch Operation



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Important Concepts

- Process cell
- Process unit
- Batch
- General recipe
- Control recipe
- Phase

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Batch – from the start

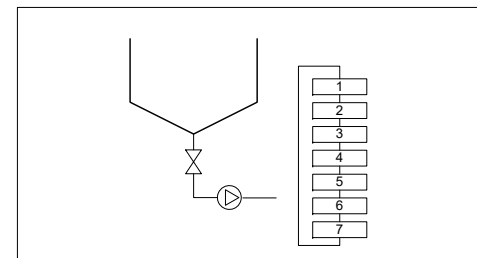
- The customer has a manufacturing procedure (recipe)
- How to structure the software ?

Add 10 l of water
Add 5 l of ethanol
Mix 3 min, 300 rpm
Add 2 l of salt
Mix 2 min, 1000 rpm
Heat until 45° C
Empty after 5 min

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Batch (2)

Identify a sequence of operations in the unit



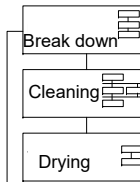
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Batch (3)

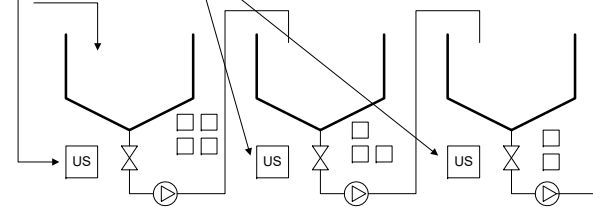
- Identify phases and parameters for each unit
 - Raw material (sort, volume)
 - Mixing (time, speed)
 - Temperature control (setpoint value)
 - Empty (time)
- Write the executing of the recipe

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Batch (4) - several units



- Identify the **equipment** need
- Write **recipe**
- Start the production



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